

Digital Logic Applications And Design John M Yarbrough

THANK YOU UNQUESTIONABLY MUCH FOR DOWNLOADING **DIGITAL LOGIC APPLICATIONS AND DESIGN JOHN M YARBROUGH**. MOST LIKELY YOU HAVE KNOWLEDGE THAT, PEOPLE HAVE SEEN NUMEROUS TIMES FOR THEIR FAVORITE BOOKS CONSIDERING THIS **DIGITAL LOGIC APPLICATIONS AND DESIGN JOHN M YARBROUGH**, BUT END TAKING PLACE IN HARMFUL DOWNLOADS.

RATHER THAN ENJOYING A FINE PDF SUBSEQUENT TO A MUG OF COFFEE IN THE AFTERNOON, OTHERWISE THEY JUGGLED AS SOON AS SOME HARMFUL VIRUS INSIDE THEIR COMPUTER. **DIGITAL LOGIC APPLICATIONS AND DESIGN JOHN M YARBROUGH** IS AFFABLE IN OUR DIGITAL LIBRARY AN ONLINE ENTRY TO IT IS SET AS PUBLIC CONSEQUENTLY YOU CAN DOWNLOAD IT INSTANTLY. OUR DIGITAL LIBRARY SAVES IN COMBINATION COUNTRIES, ALLOWING YOU TO GET THE MOST LESS LATENCY ERA TO DOWNLOAD ANY OF OUR BOOKS BEHIND THIS ONE. MERELY SAID, THE **DIGITAL LOGIC APPLICATIONS AND DESIGN JOHN M YARBROUGH** IS UNIVERSALLY COMPATIBLE AS SOON AS ANY DEVICES TO READ.

DIGITAL LOGIC JOHN M. YARBROUGH 1998 DESIGNED FOR THE FIRST DIGITAL COURSE FOR FOUR-YEAR ELECTRICAL ENGINEERING MAJORS AND FOR THE SECOND COURSE (FOLLOWING BASIC LOGIC) FOR FOUR-YEAR ELECTRICAL AND ELECTRONIC ENGINEERING TECHNOLOGY MAJORS. FEATURES A CLASSICAL APPROACH TO THE SUBJECT. PROVIDES A THOROUGH EXPLANATION OF THE DESIGN PROCESS. INCLUDES REAL-WORLD EXAMPLES WITH REAL-WORLD PARTS. EXTENSIVE PROBLEM SETS. PLD COVERAGE.

COMPUTER LITERATURE BIBLIOGRAPHY UNITED STATES. NATIONAL BUREAU OF STANDARDS 1965

DESIGNING FOR THE DIGITAL AGE KIM GOODWIN 2011-03-25 WHETHER YOU'RE DESIGNING CONSUMER ELECTRONICS, MEDICAL DEVICES, ENTERPRISE WEB APPS, OR NEW WAYS TO CHECK OUT AT THE SUPERMARKET, TODAY'S DIGITALLY-ENABLED PRODUCTS AND SERVICES PROVIDE BOTH GREAT OPPORTUNITIES TO DELIVER COMPELLING USER EXPERIENCES AND GREAT RISKS OF DRIVING YOUR CUSTOMERS CRAZY WITH COMPLICATED, CONFUSING TECHNOLOGY. DESIGNING SUCCESSFUL PRODUCTS AND SERVICES IN THE DIGITAL AGE REQUIRES A MULTI-DISCIPLINARY TEAM WITH EXPERTISE IN INTERACTION DESIGN, VISUAL DESIGN, INDUSTRIAL DESIGN, AND OTHER DISCIPLINES. IT ALSO TAKES THE ABILITY TO COME UP WITH THE BIG IDEAS THAT MAKE A DESIRABLE PRODUCT OR SERVICE, AS WELL AS THE SKILL AND PERSEVERANCE TO EXECUTE ON THE THOUSAND SMALL IDEAS THAT GET YOUR DESIGN INTO THE HANDS OF USERS. IT REQUIRES EXPERTISE IN PROJECT MANAGEMENT, USER RESEARCH, AND CONSENSUS-BUILDING. THIS COMPREHENSIVE, FULL-COLOR VOLUME ADDRESSES ALL OF THESE AND MORE WITH DETAILED HOW-TO INFORMATION, REAL-LIFE EXAMPLES, AND EXERCISES. TOPICS INCLUDE ASSEMBLING A DESIGN TEAM, PLANNING AND CONDUCTING USER RESEARCH, ANALYZING YOUR DATA AND TURNING IT INTO PERSONAS, USING SCENARIOS TO DRIVE REQUIREMENTS DEFINITION AND DESIGN, COLLABORATING IN DESIGN MEETINGS, EVALUATING AND ITERATING YOUR DESIGN, AND DOCUMENTING FINISHED DESIGN IN A WAY THAT WORKS FOR ENGINEERS AND

STAKEHOLDERS ALIKE.

THE BRITISH NATIONAL BIBLIOGRAPHY ARTHUR JAMES WELLS 2007
SWITCHING THEORY AND LOGIC DESIGN A. ANAND KUMAR 2014-03-06 THIS COMPREHENSIVE TEXT ON SWITCHING THEORY AND LOGIC DESIGN IS DESIGNED FOR THE UNDERGRADUATE STUDENTS OF ELECTRONICS AND COMMUNICATION ENGINEERING, ELECTRICAL AND ELECTRONICS ENGINEERING, ELECTRONICS AND INSTRUMENTATION ENGINEERING, TELECOMMUNICATION ENGINEERING, COMPUTER SCIENCE AND ENGINEERING, AND INFORMATION TECHNOLOGY. IT WILL ALSO BE USEFUL TO AMIE, IETE AND DIPLOMA STUDENTS. WRITTEN IN A STUDENT-FRIENDLY STYLE, THIS BOOK, NOW IN ITS SECOND EDITION, PROVIDES AN IN-DEPTH KNOWLEDGE OF SWITCHING THEORY AND THE DESIGN TECHNIQUES OF DIGITAL CIRCUITS. STRIKING A BALANCE BETWEEN THEORY AND PRACTICE, IT COVERS TOPICS RANGING FROM NUMBER SYSTEMS, BINARY CODES, LOGIC GATES AND BOOLEAN ALGEBRA TO MINIMIZATION USING K-MAPS AND TABULAR METHOD, DESIGN OF COMBINATIONAL LOGIC CIRCUITS, SYNCHRONOUS AND ASYNCHRONOUS SEQUENTIAL CIRCUITS, AND ALGORITHMIC STATE MACHINES. THE BOOK DISCUSSES THRESHOLD GATES AND PROGRAMMABLE LOGIC DEVICES (PLDs). IN ADDITION, IT ELABORATES ON FLIP-FLOPS AND SHIFT REGISTERS. EACH CHAPTER INCLUDES SEVERAL FULLY WORKED-OUT EXAMPLES SO THAT THE STUDENTS GET A THOROUGH GROUNDING IN RELATED DESIGN CONCEPTS. SHORT QUESTIONS WITH ANSWERS, REVIEW QUESTIONS, FILL IN THE BLANKS, MULTIPLE CHOICE QUESTIONS AND PROBLEMS ARE PROVIDED AT THE END OF EACH CHAPTER. THESE HELP THE STUDENTS TEST THEIR LEVEL OF UNDERSTANDING OF THE SUBJECT AND PREPARE FOR EXAMINATIONS CONFIDENTLY. NEW TO THIS EDITION • VHDL PROGRAMS AT THE END OF EACH CHAPTER • COMPLETE ANSWERS WITH FIGURES • SEVERAL NEW PROBLEMS WITH ANSWERS

DIGITAL LOGIC DESIGN GUY EVEN 2012-10-08 THIS TEXTBOOK, BASED ON THE AUTHOR'S FIFTEEN YEARS OF TEACHING, IS A COMPLETE TEACHING TOOL FOR TURNING STUDENTS INTO LOGIC DESIGNERS IN ONE SEMESTER. EACH CHAPTER DESCRIBES NEW CONCEPTS, GIVING

EXTENSIVE APPLICATIONS AND EXAMPLES. ASSUMING NO PRIOR KNOWLEDGE OF DISCRETE MATHEMATICS, THE AUTHORS INTRODUCE ALL BACKGROUND IN PROPOSITIONAL LOGIC, ASYMPTOTICS, GRAPHS, HARDWARE AND ELECTRONICS. IMPORTANT FEATURES OF THE PRESENTATION ARE: • ALL MATERIAL IS PRESENTED IN FULL DETAIL. EVERY DESIGNED CIRCUIT IS FORMALLY SPECIFIED AND IMPLEMENTED, THE CORRECTNESS OF THE IMPLEMENTATION IS PROVED, AND THE COST AND DELAY ARE ANALYZED • ALGORITHMIC SOLUTIONS ARE OFFERED FOR LOGICAL SIMULATION, COMPUTATION OF PROPAGATION DELAY AND MINIMUM CLOCK PERIOD • CONNECTIONS ARE DRAWN FROM THE PHYSICAL ANALOG WORLD TO THE DIGITAL ABSTRACTION • THE LANGUAGE OF GRAPHS IS USED TO DESCRIBE FORMULAS AND CIRCUITS • HUNDREDS OF FIGURES, EXAMPLES AND EXERCISES ENHANCE UNDERSTANDING. THE EXTENSIVE WEBSITE ([HTTP://WWW.ENG.TAU.AC.IL/~GUY/EVEN-MEDINA/](http://www.eng.tau.ac.il/~guy/Even-Medina/)) INCLUDES TEACHING SLIDES, LINKS TO LOGISIM AND A DLX ASSEMBLY SIMULATOR.

ENGINEERING DIGITAL DESIGN RICHARD F. TINDER 2000-01 "ENGINEERING DIGITAL DESIGN" PROVIDES THE MOST EXTENSIVE COVERAGE OF ANY AVAILABLE TEXTBOOK IN DIGITAL LOGIC AND DESIGN. MODERN NOTATION COMBINES WITH A STATE-OF-THE-ART TREATMENT OF THE MOST IMPORTANT SUBJECTS IN DIGITAL DESIGN TO PROVIDE THE STUDENT WITH THE BACKGROUND NEEDED TO ENTER INDUSTRY OR GRADUATE STUDY AT A COMPETITIVE LEVEL. SOFTWARE PROGRAMS, INCLUDING A LOGIC MINIMIZER AND A LOGIC SIMULATOR, ARE PROVIDED ON A CD-ROM AND INCLUDE DETAILED INSTRUCTIONS FOR USE.

DIGITAL ELECTRONICS CHRISTOPHER E. STRANGIO 1980 LOGIC CONCEPTS; BOOLEAN ALGEBRA; COMBINATIONAL LOGIC; BINARY NUMBER OPERATIONS; FLIP-FLOPS; COUNTER ANALYSIS AND DESIGN; SEQUENTIAL CIRCUITS; DIGITAL CIRCUIT FAULT ANALYSIS; ANALOG-DIGITAL CONVERSION; COMPUTERS AND MICROPROCESSORS.

DIGITAL DESIGN M. MORRIS MANO 2013 FOR COURSES ON DIGITAL DESIGN IN AN ELECTRICAL ENGINEERING, COMPUTER ENGINEERING, OR COMPUTER SCIENCE DEPARTMENT. DIGITAL DESIGN, FIFTH EDITION IS A MODERN UPDATE OF THE CLASSIC AUTHORITATIVE TEXT ON DIGITAL DESIGN. THIS BOOK TEACHES THE BASIC CONCEPTS OF DIGITAL DESIGN IN A CLEAR, ACCESSIBLE MANNER. THE BOOK PRESENTS THE BASIC TOOLS FOR THE DESIGN OF DIGITAL CIRCUITS AND PROVIDES PROCEDURES SUITABLE FOR A VARIETY OF DIGITAL APPLICATIONS.

ELECTRICAL, ELECTRONICS AND COMPUTER ENGINEERING FOR SCIENTISTS AND ENGINEERS KRISHNAMURTHY 2007 THIS BOOK PRESENTS A LUCID AND SYSTEMATIC EXPOSITION OF THE BASIC PRINCIPLES INVOLVED IN ELECTRICAL AND ELECTRONICS ENGINEERING. A WIDE SPECTRUM OF CONCEPTS IS COVERED, RANGING FROM THE BASIC PRINCIPLES OF ELECTRIC CIRCUITS TO THE ADVANCED AREA OF MICROPROCESSORS. THE FUNDAMENTAL CONCEPTS ARE EXPLAINED IN SUFFICIENT DETAIL AND ARE ADEQUATELY ILLUSTRATED THROUGH SUITABLE SOLVED EXAMPLES. THIS EDITION INCLUDES NEW CHAPTERS ON * DC MACHINES * AC MACHINES * ELECTRICAL MEASURING INSTRUMENTS * COMMUNICATION SYSTEMS * OSCILLATORS THE DISCUSSION OF SEVERAL OTHER TOPICS HAS ALSO BEEN SUITABLY REVISED AND UPDATED. THE BOOK WOULD SERVE AS AN EXCELLENT FOR UNDERGRADUATE ENGINEERING AND DIPLOMA STUDENTS OF ALL DISCIPLINES. AMIE CANDIDATES AND

PRACTISING ENGINEERS WOULD ALSO FIND IT EXTREMELY USEFUL.

DIGITAL LOGIC JOHN M. YARBROUGH 1997 DIGITAL LOGIC OFFERS THE RIGHT BALANCE OF CLASSICAL AND UP-TO-DATE TREATMENT OF COMBINATIONAL AND SEQUENTIAL LOGIC DESIGN FOR A FIRST DIGITAL LOGIC DESIGN CLASS. THE AUTHOR PROVIDES A THOROUGH EXPLANATION OF THE DESIGN PROCESS, INCLUDING COMPLETELY WORKED EXAMPLES BEGINNING WITH SIMPLE EXAMPLES AND GOING ON TO PROBLEMS OF INCREASING COMPLEXITY. THIS TEXT CONTAINS PLD (PROGRAMMABLE LOGIC DESIGN) COVERAGE. CHAPTER 9 DEVELOPS COMPLETE, WORKED EPROM, PLA, AND EPLD DESIGN EXAMPLES. THE PROBLEMS ARE DEVELOPED IN CHAPTER 7 AS STANDARD DESIGNS USING SSI AND MSI DEVICES SO THAT YOUR STUDENTS CAN SEE THE DIFFERENCE BETWEEN THE TWO APPROACHES.

FUNDAMENTALS OF DIGITAL LOGIC AND MICROCONTROLLERS M. RAFIQUZZAMAN 2014-11-06 UPDATED TO REFLECT THE LATEST ADVANCES IN THE FIELD, THE SIXTH EDITION OF FUNDAMENTALS OF DIGITAL LOGIC AND MICROCONTROLLERS FURTHER ENHANCES ITS REPUTATION AS THE MOST ACCESSIBLE INTRODUCTION TO THE BASIC PRINCIPLES AND TOOLS REQUIRED IN THE DESIGN OF DIGITAL SYSTEMS. FEATURES UPDATES AND REVISION TO MORE THAN HALF OF THE MATERIAL FROM THE PREVIOUS EDITION OFFERS AN ALL-ENCOMPASSING FOCUS ON THE AREAS OF COMPUTER DESIGN, DIGITAL LOGIC, AND DIGITAL SYSTEMS, UNLIKE OTHER TEXTS IN THE MARKETPLACE WRITTEN WITH CLEAR AND CONCISE EXPLANATIONS OF FUNDAMENTAL TOPICS SUCH AS NUMBER SYSTEM AND BOOLEAN ALGEBRA, AND SIMPLIFIED EXAMPLES AND TUTORIALS UTILIZING THE PIC18F4321 MICROCONTROLLER COVERS AN ENHANCED VERSION OF BOTH COMBINATIONAL AND SEQUENTIAL LOGIC DESIGN, BASICS OF COMPUTER ORGANIZATION, AND MICROCONTROLLERS
MA-2, MICROPROCESSOR APPLICATIONS EXPERIMENTS: APPENDICES (LAB HANDBOOK) HOWARD BOYET 1979

BOOLEAN ALGEBRA AND ITS APPLICATIONS J. ELDON WHITESITT 2012-05-24 INTRODUCTORY TREATMENT BEGINS WITH SET THEORY AND FUNDAMENTALS OF BOOLEAN ALGEBRA, PROCEEDING TO CONCISE ACCOUNTS OF APPLICATIONS TO SYMBOLIC LOGIC, SWITCHING CIRCUITS, RELAY CIRCUITS, BINARY ARITHMETIC, AND PROBABILITY THEORY. 1961 EDITION.

COMPUTER ARITHMETIC MIRCEA VLADU 2012-09-14 THE SUBJECT OF THIS BOOK IS THE ANALYSIS AND DESIGN OF DIGITAL DEVICES THAT IMPLEMENT COMPUTER ARITHMETIC. THE BOOK'S PRESENTATION OF HIGH-LEVEL DETAIL, DESCRIPTIONS, FORMALISMS AND DESIGN PRINCIPLES MEANS THAT IT CAN SUPPORT MANY RESEARCH ACTIVITIES IN THIS FIELD, WITH AN EMPHASIS ON BRIDGING THE GAP BETWEEN ALGORITHM OPTIMIZATION AND HARDWARE IMPLEMENTATION. THE AUTHOR PROVIDES A UNIFIED VIEW LINKING THE DOMAINS OF DIGITAL DESIGN AND ARITHMETIC ALGORITHMS, BASED ON ORIGINAL FORMALISMS AND HARDWARE DESCRIPTION LANGUAGES. A FEATURE OF THE BOOK IS THE LARGE NUMBER OF EXAMPLES AND THE IMPLEMENTATION DETAILS PROVIDED. WHILE THE AUTHOR DOES NOT AVOID HIGH-LEVEL DETAILS, PROVIDING FOR EXAMPLE GATE-LEVEL DESIGNS FOR ALL MATRIX/COMBINATIONAL ARITHMETIC STRUCTURES. THE BOOK IS SUITABLE FOR RESEARCHERS AND STUDENTS ENGAGED

WITH HARDWARE DESIGN IN COMPUTER SCIENCE AND ENGINEERING. A FEATURE OF THE BOOK IS THE LARGE NUMBER OF EXAMPLES AND THE IMPLEMENTATION DETAILS PROVIDED. WHILE THE AUTHOR DOES NOT AVOID HIGH-LEVEL DETAILS, PROVIDING FOR EXAMPLE GATE-LEVEL DESIGNS FOR ALL MATRIX/COMBINATIONAL ARITHMETIC STRUCTURES. THE BOOK IS SUITABLE FOR RESEARCHERS AND STUDENTS ENGAGED WITH HARDWARE DESIGN IN COMPUTER SCIENCE AND ENGINEERING.

FOUNDATIONS OF DIGITAL LOGIC DESIGN GIDEON LANGHOLZ 1998 THIS TEXT IS INTENDED FOR A FIRST COURSE IN DIGITAL LOGIC DESIGN, AT THE SOPHOMORE OR JUNIOR LEVEL, FOR ELECTRICAL ENGINEERING, COMPUTER ENGINEERING AND COMPUTER SCIENCE PROGRAMS, AS WELL AS FOR A NUMBER OF OTHER DISCIPLINES SUCH AS PHYSICS AND MATHEMATICS. THE BOOK CAN ALSO BE USED FOR SELF-STUDY OR FOR REVIEW BY PRACTICING ENGINEERS AND COMPUTER SCIENTISTS NOT INTIMATELY FAMILIAR WITH THE SUBJECT. AFTER COMPLETING THIS TEXT, THE STUDENT SHOULD BE PREPARED FOR A SECOND (ADVANCED) COURSE IN DIGITAL DESIGN, SWITCHING AND AUTOMATA THEORY, MICROPROCESSORS OR COMPUTER ORGANIZATION.

DIGITAL LOGIC CIRCUIT ANALYSIS AND DESIGN (SECOND EDITION) VICTOR PETER NELSON 2020

MATERIALS PRESENTED AT THE MU-SPIN EIGHTH ANNUAL USER'S CONFERENCE 1999

PRINCIPLES OF COMPUTER HARDWARE ALAN CLEMENTS 2006-02-09 THE FOURTH EDITION OF THIS WORK PROVIDES A READABLE, TUTORIAL BASED INTRODUCTION TO THE SUBJECT OF COMPUTER HARDWARE FOR UNDERGRADUATE COMPUTER SCIENTISTS AND ENGINEERS AND INCLUDES A COMPANION WEBSITE TO GIVE LECTURERS ADDITIONAL NOTES.

DIGITAL LOGIC APPLICATIONS AND DESIGN YARBROUGH

DIGITAL LOGIC DESIGN MING-BO LIN 2016-09-05 DIGITAL LOGIC DESIGN IS A COMPREHENSIVE TEXTBOOK, WHICH AIMS TO PROVIDE ENTRY-LEVEL READERS A QUICK START TO THE FIELD OF DIGITAL LOGIC DESIGN SO AS TO FACILITATE THEM WITH THE CAPABILITY SUITABLE FOR THE VERSATILITY OF SOCIAL CHANGE AND INTERDISCIPLINARY LEARNING. THIS TEXTBOOK CAN BE USED AS A TEXTBOOK FOR CLASSROOM USE IN THE FIELDS OF ELECTRONICS, ELECTRICAL, COMPUTER SCIENCE, INFORMATION ENGINEERING, MECHANICAL, AND SO ON. THE SALIENT FEATURES OF THIS TEXTBOOK ARE AS FOLLOWS: -- INTRODUCE INCREMENTALLY THE PRINCIPLES OF DIGITAL LOGIC DESIGN AND EXEMPLIFY EACH BASIC THEME AND CONCEPT WITH ABUNDANT ILLUSTRATIONS. -- DETAIL DESIGN PRINCIPLES OF VARIOUS COMBINATIONAL MODULES, INCLUDING DECODERS, ENCODERS, MULTIPLEXERS, DEMULTIPLEXERS, ARITHMETIC CIRCUITS, AND SO ON. -- INTRODUCE DESIGN PRINCIPLES OF VARIOUS SEQUENTIAL MODULES, INCLUDING COUNTERS, REGISTERS, SHIFT REGISTERS, SEQUENCE GENERATORS, ETC. -- ADDRESS THE STRUCTURES, FEATURES, AND APPLICATIONS OF PLD/FPGA DEVICES. -- EXEMPLIFY APPLICATIONS OF CPLD/FPGA DEVICES WITH VERILOG HDL MODULES. -- PROVIDE 20 BASIC AND APPLICATION EXPERIMENTS OF DIGITAL

LOGIC TO HELP READERS VERIFY THE CONSISTENCE OF DIGITAL LOGIC BETWEEN PRINCIPLES AND PRACTICE. -- INCLUDE AN ABUNDANCE OF REVIEW QUESTIONS IN EACH SECTION TO HELP READERS EVALUATE THEIR UNDERSTANDINGS ABOUT THE SECTION. -- DEAL WITH VERILOG HDL CONCISELY IN RELEVANT SECTIONS SO AS TO MAKE THE READER UNDERSTAND HOW TO DESCRIBE A LOGIC CIRCUIT IN VERILOG HDL PRECISELY. DIGITAL LOGIC DESIGN IS AN IDEAL TEXTBOOK FOR THE DIGITAL LOGIC DESIGN COURSE IN THE FIELDS OF ELECTRONICS, ELECTRICAL, COMPUTER SCIENCE, INFORMATION ENGINEERING, MECHANICAL, ETC, OR SERVES AS A VALUABLE REFERENCE BOOK FOR SELF-STUDY.

DIGITAL LOGIC DESIGN BRIAN HOLDSWORTH 1982

INTRODUCTION TO DIGITAL SYSTEMS MOHAMMED FERDJALLAH 2011-06-24 A UNIQUE GUIDE TO USING BOTH MODELING AND SIMULATION IN DIGITAL SYSTEMS DESIGN DIGITAL SYSTEMS DESIGN REQUIRES RIGOROUS MODELING AND SIMULATION ANALYSIS THAT ELIMINATES DESIGN RISKS AND POTENTIAL HARM TO USERS. INTRODUCTION TO DIGITAL SYSTEMS: MODELING, SYNTHESIS, AND SIMULATION USING VHDL INTRODUCES THE APPLICATION OF MODELING AND SYNTHESIS IN THE EFFECTIVE DESIGN OF DIGITAL SYSTEMS AND EXPLAINS APPLICABLE ANALYTICAL AND COMPUTATIONAL METHODS. THROUGH STEP-BY-STEP EXPLANATIONS AND NUMEROUS EXAMPLES, THE AUTHOR EQUIPS READERS WITH THE TOOLS NEEDED TO MODEL, SYNTHESIZE, AND SIMULATE DIGITAL PRINCIPLES USING VERY HIGH SPEED INTEGRATED CIRCUIT HARDWARE DESCRIPTION LANGUAGE (VHDL) PROGRAMMING.

EXTENSIVELY CLASSROOM-TESTED TO ENSURE A FLUID PRESENTATION, THIS BOOK PROVIDES A COMPREHENSIVE OVERVIEW OF THE TOPIC BY INTEGRATING THEORETICAL PRINCIPLES, DISCRETE MATHEMATICAL MODELS, COMPUTER SIMULATIONS, AND BASIC METHODS OF ANALYSIS. TOPICAL COVERAGE INCLUDES: DIGITAL SYSTEMS MODELING AND SIMULATION INTEGRATED LOGIC BOOLEAN ALGEBRA AND LOGIC LOGIC FUNCTION OPTIMIZATION NUMBER SYSTEMS COMBINATIONAL LOGIC VHDL DESIGN CONCEPTS SEQUENTIAL AND SYNCHRONOUS SEQUENTIAL LOGIC EACH CHAPTER BEGINS WITH LEARNING OBJECTIVES THAT OUTLINE KEY CONCEPTS THAT FOLLOW, AND ALL DISCUSSIONS CONCLUDE WITH PROBLEM SETS THAT ALLOW READERS TO TEST THEIR COMPREHENSION OF THE PRESENTED MATERIAL. THROUGHOUT THE BOOK, VHDL SAMPLE CODES ARE USED TO ILLUSTRATE CIRCUIT DESIGN, PROVIDING GUIDANCE NOT ONLY ON HOW TO LEARN AND MASTER VHDL PROGRAMMING, BUT ALSO HOW TO MODEL AND SIMULATE DIGITAL CIRCUITS. INTRODUCTION TO DIGITAL SYSTEMS IS AN EXCELLENT BOOK FOR COURSES IN MODELING AND SIMULATION, OPERATIONS RESEARCH, ENGINEERING, AND COMPUTER SCIENCE AT THE UPPER-UNDERGRADUATE AND GRADUATE LEVELS. THE BOOK ALSO SERVES AS A VALUABLE RESOURCE FOR RESEARCHERS AND PRACTITIONERS IN THE FIELDS OF OPERATIONS RESEARCH, MATHEMATICAL MODELING, SIMULATION, ELECTRICAL ENGINEERING, AND COMPUTER SCIENCE.

INDIAN BOOKS IN PRINT 2003

DIGITAL DESIGNING WITH PROGRAMMABLE LOGIC DEVICES JOHN W. CARTER 1997 THE PURPOSE OF THIS TEXT IS TO USE HANDS-ON METHODOLOGY TO PRESENT PROGRAMMABLE LOGIC DEVICES FROM A VIEWPOINT WHICH WILL PREPARE THE STUDENT FOR APPLICATION

WITHIN THE DIGITAL DESIGN INDUSTRY. THE KNOWLEDGE OF STATE MACHINES AND THE ABILITY TO APPLY THEM TO CONTROL SITUATIONS ARE VITAL TO THE OVERALL EDUCATION OF THE DIGITAL DESIGNER. CONCENTRATING ON PROGRAMMABLE LOGIC DEVICES, IT PREPARES THE READER TO BE A MORE VALUABLE PART OF THE DESIGN TEAM. AN INDUCTIVE/APPLICATION APPROACH TO THE USE OF PROGRAMMABLE LOGIC DEVICES IN DIGITAL ELECTRONIC DESIGN IS APPLICATION-ORIENTED RATHER THAN THEORETICAL. THIS RESULTS IN THE ACQUISITION OF LEARNED, REPEATABLE SKILLS. THE TEXT CONTAINS NUMEROUS EXAMPLES AND COMPLETELY WORKED PROBLEMS WITH INTEGRATED TEXT, DESCRIBING EACH STEP OF THE DESIGN PROCESS.

PATTERN LANGUAGES OF PROGRAM DESIGN JAMES O. COPLIEN 1995 THE FIRST CONFERENCE ON PATTERN LANGUAGES OF PROGRAM DESIGN (PLOP) WAS A WATERSHED EVENT THAT GAVE A PUBLIC VOICE TO THE SOFTWARE DESIGN PATTERN MOVEMENT. SEVENTY SOFTWARE PROFESSIONALS FROM AROUND THE WORLD WORKED TOGETHER TO CAPTURE AND REFINES SOFTWARE EXPERIENCE THAT EXEMPLIFIES THE ELUSIVE QUALITY CALLED "GOOD DESIGN." THIS VOLUME IS THE RESULT OF THAT WORK--A BROAD COMPENDIUM OF THIS NEW GENRE OF SOFTWARE LITERATURE. PATTERNS ARE A LITERARY FORM THAT TAKE INSPIRATION FROM LITERATE PROGRAMMING, FROM A DESIGN MOVEMENT OF THE SAME NAME IN CONTEMPORARY ARCHITECTURE, AND FROM THE PRACTICES COMMON TO THE AGELESS LITERATURE OF ANY CULTURE. THE GOAL OF PATTERN LITERATURE IS TO HELP PROGRAMMERS RESOLVE THE COMMON DIFFICULT PROBLEMS ENCOUNTERED IN DESIGN AND PROGRAMMING. SPANNING DISCIPLINES AS BROAD AS CLIENT/SERVER PROGRAMMING, DISTRIBUTED PROCESSING, ORGANIZATIONAL DESIGN, SOFTWARE REUSE, AND HUMAN INTERFACE DESIGN, THIS VOLUME ENCODES DESIGN EXPERTISE THAT TOO OFTEN REMAINS LOCKED IN THE MINDS OF EXPERT ARCHITECTS. BY CAPTURING THESE EXPERT PRACTICES AS PROBLEM-SOLUTION PAIRS SUPPORTED WITH A DISCUSSION OF THE FORCES THAT SHAPE ALTERNATIVE SOLUTION CHOICES, AND RATIONALES THAT CLARIFY THE ARCHITECTS' INTENTS, THESE PATTERNS CONVEY THE ESSENCE OF GREAT SOFTWARE DESIGNS.

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BASIC CONCEPTS IN DIGITAL ELECTRONICS AND LOGIC DESIGN ER JAWAD AHMAD DAR

DIGITAL LOGIC AND COMPUTER ORGANIZATION V. RAJARAMAN 2006-01-01

THIS INTRODUCTORY TEXT ON 'DIGITAL LOGIC AND COMPUTER ORGANIZATION' PRESENTS A LOGICAL TREATMENT OF ALL THE FUNDAMENTAL CONCEPTS NECESSARY TO UNDERSTAND THE ORGANIZATION AND DESIGN OF A COMPUTER. IT IS DESIGNED TO COVER THE REQUIREMENTS OF A FIRST-COURSE IN COMPUTER ORGANIZATION FOR UNDERGRADUATE COMPUTER SCIENCE, ELECTRONICS, OR MCA STUDENTS. BEGINNING FROM FIRST PRINCIPLES, THE TEXT GUIDES STUDENTS THROUGH TO A STAGE WHERE THEY ARE ABLE TO DESIGN AND BUILD A SMALL COMPUTER WITH AVAILABLE IC CHIPS. STARTING WITH THE FOUNDATION MATERIAL ON DATA REPRESENTATION, COMPUTER ARITHMETIC AND COMBINATORIAL AND SEQUENTIAL CIRCUIT DESIGN, THE TEXT EXPLAINS ALU DESIGN AND INCLUDES A DISCUSSION ON AN ALU IC CHIP. IT ALSO DISCUSSES ALGORITHMIC STATE MACHINE AND ITS REPRESENTATION USING A

HARDWARE DESCRIPTION LANGUAGE BEFORE SHIFTING TO COMPUTER ORGANIZATION. THE EVOLUTIONARY DEVELOPMENT OF A SMALL HYPOTHETICAL COMPUTER IS DESCRIBED ILLUSTRATING HARDWARE-SOFTWARE TRADE-OFF IN COMPUTER ORGANIZATION. ITS INSTRUCTION SET IS DESIGNED GIVING REASONS WHY EACH NEW INSTRUCTION IS INTRODUCED. THIS IS FOLLOWED BY A DESCRIPTION OF THE GENERAL FEATURES OF A CPU, ORGANIZATION OF MAIN MEMORY AND I/O SYSTEMS. THE BOOK CONCLUDES WITH A CHAPTER DESCRIBING THE FEATURES OF A REAL COMPUTER, NAMELY THE INTEL PENTIUM. AN APPENDIX DESCRIBES A NUMBER OF LABORATORY EXPERIMENTS WHICH CAN BE PUT TOGETHER BY STUDENTS, CULMINATING IN THE DESIGN OF A TOY COMPUTER. KEY FEATURES • SELF-CONTAINED PRESENTATION OF DIGITAL LOGIC AND COMPUTER ORGANIZATION WITH MINIMAL PRE-REQUISITES • LARGE NUMBER OF EXAMPLES PROVIDED THROUGHOUT THE BOOK • EACH CHAPTER BEGINS WITH LEARNING GOALS AND ENDS WITH A SUMMARY TO AID SELF-STUDY BY STUDENTS.

FORTHCOMING BOOKS ROSE ARNY 1998

FUNDAMENTALS OF DIGITAL LOGIC AND MICROCOMPUTER DESIGN M. RAFIQUZZAMAN

2005-06-06 FUNDAMENTALS OF DIGITAL LOGIC AND MICROCOMPUTER DESIGN, HAS LONG BEEN HAILED FOR ITS CLEAR AND SIMPLE PRESENTATION OF THE PRINCIPLES AND BASIC TOOLS REQUIRED TO DESIGN TYPICAL DIGITAL SYSTEMS SUCH AS MICROCOMPUTERS. IN THIS FIFTH EDITION, THE AUTHOR FOCUSES ON COMPUTER DESIGN AT THREE LEVELS: THE DEVICE LEVEL, THE LOGIC LEVEL, AND THE SYSTEM LEVEL. BASIC TOPICS ARE COVERED, SUCH AS NUMBER SYSTEMS AND BOOLEAN ALGEBRA, COMBINATIONAL AND SEQUENTIAL LOGIC DESIGN, AS WELL AS MORE ADVANCED SUBJECTS SUCH AS ASSEMBLY LANGUAGE PROGRAMMING AND MICROPROCESSOR-BASED SYSTEM DESIGN. NUMEROUS EXAMPLES ARE PROVIDED THROUGHOUT THE TEXT. COVERAGE INCLUDES: DIGITAL CIRCUITS AT THE GATE AND FLIP-FLOP LEVELS ANALYSIS AND DESIGN OF COMBINATIONAL AND SEQUENTIAL CIRCUITS MICROCOMPUTER ORGANIZATION, ARCHITECTURE, AND PROGRAMMING CONCEPTS DESIGN OF COMPUTER INSTRUCTION SETS, CPU, MEMORY, AND I/O SYSTEM DESIGN FEATURES ASSOCIATED WITH POPULAR MICROPROCESSORS FROM INTEL AND MOTOROLA FUTURE PLANS IN MICROPROCESSOR DEVELOPMENT AN INSTRUCTOR'S MANUAL, AVAILABLE UPON REQUEST ADDITIONALLY, THE ACCOMPANYING CD-ROM, CONTAINS STEP-BY-STEP PROCEDURES FOR INSTALLING AND USING ALTERA QUARTUS II SOFTWARE, MASM 6.11 (8086), AND 68ASMSIM (68000), PROVIDES VALUABLE SIMULATION RESULTS VIA SCREEN SHOTS. FUNDAMENTALS OF DIGITAL LOGIC AND MICROCOMPUTER DESIGN IS AN ESSENTIAL REFERENCE THAT WILL PROVIDE YOU WITH THE FUNDAMENTAL TOOLS YOU NEED TO DESIGN TYPICAL DIGITAL SYSTEMS.

DIGITAL DESIGN AND FABRICATION VOJIN G. OKLOBDZIJA 2017-12-19 IN RESPONSE TO TREMENDOUS GROWTH AND NEW TECHNOLOGIES IN THE SEMICONDUCTOR INDUSTRY, THIS VOLUME IS ORGANIZED INTO FIVE, INFORMATION-RICH SECTIONS. DIGITAL DESIGN AND FABRICATION SURVEYS THE LATEST ADVANCES IN COMPUTER ARCHITECTURE AND DESIGN AS WELL AS THE TECHNOLOGIES USED TO MANUFACTURE AND TEST THEM. FEATURING

CONTRIBUTIONS FROM LEADING EXPERTS, THE BOOK ALSO INCLUDES A NEW SECTION ON MEMORY AND STORAGE IN ADDITION TO A NEW CHAPTER ON NONVOLATILE MEMORY TECHNOLOGIES. DEVELOPING ADVANCED CONCEPTS, THIS SHARPLY FOCUSED BOOK— DESCRIBES NEW TECHNOLOGIES THAT HAVE BECOME DRIVING FACTORS FOR THE ELECTRONIC INDUSTRY INCLUDES NEW INFORMATION ON SEMICONDUCTOR MEMORY CIRCUITS, WHOSE DEVELOPMENT BEST ILLUSTRATES THE PHENOMENAL PROGRESS ENCOUNTERED BY THE FABRICATION AND TECHNOLOGY SECTOR CONTAINS A SECTION DEDICATED TO ISSUES RELATED TO SYSTEM POWER CONSUMPTION DESCRIBES RELIABILITY AND TESTABILITY OF COMPUTER SYSTEMS PINPOINTS TRENDS AND STATE-OF-THE-ART ADVANCES IN FABRICATION AND CMOS TECHNOLOGIES DESCRIBES PERFORMANCE EVALUATION MEASURES, WHICH ARE THE BOTTOM LINE FROM THE USER'S POINT OF VIEW DISCUSSES DESIGN TECHNIQUES USED TO CREATE MODERN COMPUTER SYSTEMS, INCLUDING HIGH-SPEED COMPUTER ARITHMETIC AND HIGH-FREQUENCY DESIGN, TIMING AND CLOCKING, AND PLL AND DLL DESIGN

DIGITAL ELECTRONIC CIRCUITS SHUQIN LOU 2019-05-20 THIS BOOK PRESENTS THREE ASPECTS OF DIGITAL CIRCUITS: DIGITAL PRINCIPLES, DIGITAL ELECTRONICS, AND DIGITAL DESIGN. THE MODERN DESIGN METHODS OF USING ELECTRONIC DESIGN AUTOMATION (EDA) ARE ALSO INTRODUCED, INCLUDING THE HARDWARE DESCRIPTION LANGUAGE (HDL), DESIGNS WITH PROGRAMMABLE LOGIC DEVICES AND LARGE SCALE INTEGRATED CIRCUIT (LSI). THE APPLICATIONS OF DIGITAL DEVICES AND INTEGRATED CIRCUITS ARE DISCUSSED IN DETAIL AS WELL.

CARBON NANOTUBE SYNTHESIS, DEVICE FABRICATION, AND CIRCUIT DESIGN FOR DIGITAL LOGIC APPLICATIONS 2010 CARBON NANOTUBE FIELD EFFECT TRANSISTOR (CNFET) TECHNOLOGY HAS RECEIVED A LOT OF ATTENTION IN THE PAST FEW YEARS AS A PROMISING EXTENSION TO SILICON-CMOS FOR FUTURE DIGITAL LOGIC INTEGRATED CIRCUITS. WHILE RECENT RESEARCH HAS ADVANCED CNFET TECHNOLOGY PAST MANY IMPORTANT MILESTONES, ROBUST AND SCALABLE SOLUTIONS MUST BE DEVELOPED TO REALIZE THE FULL POTENTIAL OF CNFETs. THUS, THIS THESIS AIMS TO DEVELOP A SUITE OF TECHNIQUES, SPANNING FROM MATERIAL SYNTHESIS TO CIRCUIT SOLUTIONS, COMPATIBLE WITH VERY-LARGE-SCALE INTEGRATION (VLSI). SPECIFICALLY, TO ENABLE THE REAL-WORLD ENGINEERING OF CARBON NANOTUBE INTEGRATED CIRCUITS, THIS THESIS PRESENTS (1) WAFER-SCALE ALIGNED CNT GROWTH, (2) WAFER-SCALE CNT TRANSFER, (3) WAFER-SCALE DEVICE AND CIRCUIT FABRICATION TECHNIQUES, AND (4) ACCNT, A VLSI-COMPATIBLE CIRCUIT DESIGN SOLUTION TO SURMOUNTING THE PROBLEM OF METALLIC CNTs. THESE TECHNIQUES CULMINATED IN THE SUCCESSFUL DEMONSTRATION OF CNT TRANSISTORS, INVERTERS, AND NAND LOGIC GATES ON A WAFER SCALE. FURTHERMORE, THIS THESIS SHEDS LIGHT ON IMPORTANT DESIGN CONSIDERATIONS FOR THE DEMONSTRATION OF A SIMPLE CNT "COMPUTER" AND SUGGESTS A FEW CRITICAL DIRECTIONS FOR FUTURE WORK IN THE FIELD OF CARBON NANOTUBE TECHNOLOGY. IN CONTRIBUTING THE ABOVE, THIS THESIS HOPES TO PROPEL CARBON NANOTUBE TECHNOLOGY FORWARD TOWARDS THE VISION OF ROBUST, LARGE-SCALE INTEGRATED CIRCUITS USING HIGH-DENSITY CARBON NANOTUBES.

DIGITAL ELECTRONICS—GATE, PSUS AND ES EXAMINATION SATISH K KARNA TEST PREP FOR DIGITAL ELECTRONICS—GATE, PSUS AND ES EXAMINATION

DIGITAL ELECTRONICS ANIL K. MAINI 2007-09-27 THE FUNDAMENTALS AND IMPLEMENTATION OF DIGITAL ELECTRONICS ARE ESSENTIAL TO UNDERSTANDING THE DESIGN AND WORKING OF CONSUMER/INDUSTRIAL ELECTRONICS, COMMUNICATIONS, EMBEDDED SYSTEMS, COMPUTERS, SECURITY AND MILITARY EQUIPMENT. DEVICES USED IN APPLICATIONS SUCH AS THESE ARE CONSTANTLY DECREASING IN SIZE AND EMPLOYING MORE COMPLEX TECHNOLOGY. IT IS THEREFORE ESSENTIAL FOR ENGINEERS AND STUDENTS TO UNDERSTAND THE FUNDAMENTALS, IMPLEMENTATION AND APPLICATION PRINCIPLES OF DIGITAL ELECTRONICS, DEVICES AND INTEGRATED CIRCUITS. THIS IS SO THAT THEY CAN USE THE MOST APPROPRIATE AND EFFECTIVE TECHNIQUE TO SUIT THEIR TECHNICAL NEED. THIS BOOK PROVIDES PRACTICAL AND COMPREHENSIVE COVERAGE OF DIGITAL ELECTRONICS, BRINGING TOGETHER INFORMATION ON FUNDAMENTAL THEORY, OPERATIONAL ASPECTS AND POTENTIAL APPLICATIONS. WITH WORKED PROBLEMS, EXAMPLES, AND REVIEW QUESTIONS FOR EACH CHAPTER, DIGITAL ELECTRONICS INCLUDES: INFORMATION ON NUMBER SYSTEMS, BINARY CODES, DIGITAL ARITHMETIC, LOGIC GATES AND FAMILIES, AND BOOLEAN ALGEBRA; AN IN-DEPTH LOOK AT MULTIPLEXERS, DE-MULTIPLEXERS, DEVICES FOR ARITHMETIC OPERATIONS, FLIP-FLOPS AND RELATED DEVICES, COUNTERS AND REGISTERS, AND DATA CONVERSION CIRCUITS; UP-TO-DATE COVERAGE OF RECENT APPLICATION FIELDS, SUCH AS PROGRAMMABLE LOGIC DEVICES, MICROPROCESSORS, MICROCONTROLLERS, DIGITAL TROUBLESHOOTING AND DIGITAL INSTRUMENTATION. A COMPREHENSIVE, MUST-READ BOOK ON DIGITAL ELECTRONICS FOR SENIOR UNDERGRADUATE AND GRADUATE STUDENTS OF ELECTRICAL, ELECTRONICS AND COMPUTER ENGINEERING, AND A VALUABLE REFERENCE BOOK FOR PROFESSIONALS AND RESEARCHERS.

COMPUTER LOGIC JOHN Y. HSU 2012-12-06 THIS BOOK PROVIDES THE READER WITH THE KEY CONCEPTS AND TECHNIQUES OF MODERN DIGITAL LOGIC DESIGN AND APPLICATIONS. THIS CONCISE TREATMENT PROVIDES ESSENTIAL DEVELOPMENT AND EXPLANATIONS FOR BOTH CLASSICAL AND MODERN TOPICS. THE MODERN TOPICS INCLUDE UNICODE, UNIPOLAR TRANSISTORS, COPPER TECHNOLOGY, FLASH MEMORY, HDL, VERILOG AND LOGIC SIMULATION SOFTWARE TOOLS. ALSO COVERED ARE COMBINATORIAL LOGIC CIRCUITS AND TRANSISTOR CIRCUITS. IT WILL BE AN ESSENTIAL RESOURCE FOR COMPUTER SCIENTISTS, LOGIC CIRCUIT DESIGNERS AND COMPUTER ENGINEERS.

DIGITAL ELECTRONICS AND LABORATORY COMPUTER EXPERIMENTS CHARLES WILKINS 2012-12-06 SCIENCE UNDERGRADUATES HAVE COME TO ACCEPT THE USE OF COMPUTERS AS COMMONPLACE. THE DAILY USE OF PORTABLE SOPHISTICATED ELECTRONIC CALCULATORS (SOME OF THEM RIVALING GENERAL-PURPOSE MINICOMPUTERS IN THEIR CAPABILITIES) HAS HASTENED THIS DEVELOPMENT. OVER THE PAST SEVERAL YEARS, COMPUTER ASSISTED EXPERIMENTATION HAS ASSUMED AN IMPORTANT ROLE IN THE EXPERIMENTAL LABORATORY. MINI- AND MICROCOMPUTER SYSTEMS HAVE BECOME AN IMPORTANT PART OF THE PHYSICAL SCIENTIST'S ARRAY OF ANALYTICAL INSTRUMENTS. PROMPTED BY OUR BELIEF THAT THIS WAS

AN INEVITABLE DEVELOPMENT, WE BEGAN SEVERAL YEARS AGO TO DEVELOP THE CURRICULAR MATERIALS PRESENTED IN THIS MANUAL. AT THE OUTSET, SEVERAL OBJECTIVES SEEMED IMPORTANT TO US FIRST, INsofar AS POSSIBLE, THE EXPERIMENTS INCLUDED SHOULD BE THOROUGHLY TESTED AND ERROR FREE. SECOND, THEY SHOULD BE COMPATIBLE WITH A VARIETY OF LABORATORY COMPUTER, DATA-ACQUISITION, AND CONTROL SYSTEMS. THIRD, LITTLE OR NO PREVIOUS BACKGROUND IN EITHER ELECTRONICS OR PROGRAMMING SHOULD BE NECESSARY. (OF COURSE, SUCH BACKGROUND WOULD BE ADVANTAGEOUS.) TO SATISFY THESE OBJECTIVES, WE DECIDED TO ADOPT A WIDESPREAD HIGH-LEVEL COMPUTER LANGUAGE, BASIC, SUITABLY MODIFIED FOR THE PURPOSE. FURTHERMORE, WE HAVE PURPOSELY AVOIDED SPECIFYING ANY PARTICULAR SYSTEM OR EQUIPMENT. RATHER, THE FUNCTIONAL CHARACTERISTICS OF BOTH HARDWARE AND SOFTWARE REQUIRED ARE STIPULATED. THE EXPERIMENTS HAVE BEEN DEVELOPED USING VARIAN 620 AND HEWLETT-PACKARD 2100 SERIES COMPUTERS, BUT WE BELIEVE THEY ARE READILY TRANSFERABLE TO OTHER COMMONLY AVAILABLE COMPUTER SYSTEMS WITH A MINIMUM OF DIFFICULTY.

DIGITAL LOGIC FOR COMPUTING JOHN SEIFFERTT 2017-05-26 THE BOOK PROVIDES A BOTTOM-UP APPROACH TO UNDERSTANDING HOW A COMPUTER WORKS AND HOW TO USE COMPUTING TO SOLVE REAL-WORLD PROBLEMS. IT COVERS THE BASICS OF DIGITAL LOGIC THROUGH THE LENS OF COMPUTER ORGANIZATION AND PROGRAMMING. THE READER SHOULD BE ABLE TO DESIGN HIS OR HER OWN COMPUTER FROM THE GROUND UP AT THE END OF THE BOOK. LOGIC SIMULATION WITH VERILOG IS USED THROUGHOUT, ASSEMBLY LANGUAGES ARE INTRODUCED AND DISCUSSED, AND THE FUNDAMENTALS OF COMPUTER ARCHITECTURE AND EMBEDDED SYSTEMS ARE TOUCHED UPON, ALL IN A COHESIVE DESIGN-DRIVEN FRAMEWORK

SUITABLE FOR CLASS OR SELF-STUDY.

POWER ELECTRONICS Ned MOHAN 2007-01-01 MARKET_Desc: • ELECTRICAL ENGINEERING STUDENTS • ELECTRICAL ENGINEERING INSTRUCTORS • POWER ELECTRONICS ENGINEERS SPECIAL FEATURES: • EASY TO FOLLOW STEP-BY-STEP IN DEPTH TREATMENT OF ALL THE THEORY. • COMPUTER SIMULATION CHAPTER DESCRIBES THE ROLE OF COMPUTER SIMULATIONS IN POWER ELECTRONICS. EXAMPLES AND PROBLEMS BASED ON PSpICE AND MATLAB ARE INCLUDED. • INTRODUCTORY CHAPTER OFFERS A REVIEW OF BASIC ELECTRICAL AND MAGNETIC CIRCUIT CONCEPTS. • A NEW CD-ROM CONTAINS THE FOLLOWING: • OVER 100 OF NEW PROBLEMS OF VARYING DEGREES OF DIFFICULTY FOR HOMEWORK ASSIGNMENTS AND SELF-LEARNING. • PSpICE-BASED SIMULATION EXAMPLES, WHICH ILLUSTRATE BASIC CONCEPTS AND HELP IN DESIGN OF CONVERTERS. • A NEWLY-DEVELOPED MAGNETIC COMPONENT DESIGN PROGRAM THAT DEMONSTRATES DESIGN TRADE-OFFS. • POWERPOINT-BASED SLIDES, WHICH WILL IMPROVE THE LEARNING EXPERIENCE AND THE EASE OF USING THE BOOK ABOUT THE BOOK: THE TEXT INCLUDES COHESIVE PRESENTATION OF POWER ELECTRONICS FUNDAMENTALS FOR APPLICATIONS AND DESIGN IN THE POWER RANGE OF 500 kW OR LESS. IT DESCRIBES A VARIETY OF PRACTICAL AND EMERGING POWER ELECTRONIC CONVERTERS MADE FEASIBLE BY THE NEW GENERATION OF POWER SEMICONDUCTOR DEVICES. TOPICS INCLUDED IN THIS BOOK ARE AN EXPANDED DISCUSSION OF DIODE RECTIFIERS AND THYRISTOR CONVERTERS AS WELL AS CHAPTERS ON HEAT SINKS, MAGNETIC COMPONENTS WHICH PRESENT A STEP-BY-STEP DESIGN APPROACH AND A COMPUTER SIMULATION OF POWER ELECTRONICS WHICH INTRODUCES NUMERICAL TECHNIQUES AND COMMONLY USED SIMULATION PACKAGES SUCH AS PSpICE, MATLAB AND EMTP.

COMPUTER LITERATURE BIBLIOGRAPHY: 1946-1963 W. W. YODEN 1965